

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

SPECIFIC SITE BACKSIDE UNDERLAYING AND
MICROMASKING METHOD FOR ELECTRICAL
CHARACTERIZATION OF SEMICONDUCTOR DEVICES

Application Number :

10/605530

Confirmation Number:

First Named Applicant:

Barbara Averill

Attorney Docket Number:

FIS920030275US1

Art Unit:

2829

Examiner:

Pavesh Patel

Search string:

(5698474 or 5786236 or 5990562 or 6078057 or 6242931 or 6245587 or 6255124
or 6329212 or 6452209).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
10	1	5698474	1997-12-16	Hurley		437	249
	2	5786236	1998-07-28	Thompson et al.		438	60
	3	5990562	1999-11-23	Vallett		257	774
	4	6078057	2000-06-20	Vallett		257	48
	5	6242931	2001-06-05	Hembree et al.	B1	324	754
	6	6245587	2001-06-12	Vallett	B1	438	18
	7	6255124	2001-07-03	Bridsley	B1	438	14
	8	6329212	2001-12-11	Dobrovolski	B1	438	15
10	9	6452209	2002-09-17	Vallett	B2	257	48

Signature

Examiner Name

Date

Pavesh Patel

12/06/04

